UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.　　　　: 7,116,441 B1
APPLICATION NO. : 09/467984
DATED　　　　　　: October 3, 2006
INVENTOR(S)　　　: Hirochika Matsuoka

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Title Page
[54] TITLE

Title: "SIGNAL PROCESSING APPARATUS IMAGE PROCESSING APPARATUS AND THEIR METHODS" should read --SIGNAL PROCESSING APPARATUS, IMAGE PROCESSING APPARATUS AND THEIR METHODS--.

SHEET 26

Figure 28, "RANG" (both occurrences) should read --RANGE--.

COLUMN 1

Line 1, "APPARATUS" should read --APPARATUS,--.
Line 16, "A computer" should read --In a computer--.

COLUMN 2

Line 5, "situation," should read --situations,--.

COLUMN 9

Line 35, "tot he" should read --to the--.

COLUMN 22

Line 14, "f())=1" should read --f(0)=1--.

COLUMN 23

Line 4, "very" should read --much--.
Line 63, "tion. Upper-limit" should read --tion. ¶ Upper-limit--.

COLUMN 24

Line 40, "Bsup" should read --B_{sup}--.
Line 43, "Bsup." should read --B_{sup}.--.
Line 46, "Bsup," should read --B_{sup},--; and
　　　　"represent" should read --represents--.
Line 48, "Bsup," should read --B_{sup},--.

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COLUMN 31

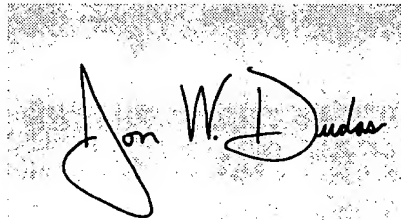Line 50, "explained the" should read --explained in the--.

COLUMN 34

Line 7, "inputting" should read --an inputting--
Line 31, "mapping" should read --a mapping--.

Signed and Sealed this

Tenth Day of April, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office